Bayesian Optimization for Broadband High-Efficiency Power Amplifier Designs

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Abstract—This paper proposes a novel, optimization-oriented strategy for the design of broadband, high-efficiency power amplifiers (PAs) using Bayesian optimization (BO). The optimization algorithm optimizes the drain waveforms by maximizing the fundamental output power while minimizing the harmonic and dissipated components. The optimization process is automated using simulation software. Circuit-based BO and electromagnetic-based (EM-based) BO are applied to design 10 W and 30 W PAs. The 10 W PA designed using circuit-based BO achieves a drain efficiency higher than 60% with output power greater than 39.8 dBm from 1.5 GHz to 2.5 GHz, while the 30 W PA designed using EM-based BO offers a drain efficiency higher than 57% with output power greater than 43.8 dBm across the band. Upon comparison of results, it is revealed that the proposed strategy outperforms a commercial electronic design automation (EDA) software’s built-in optimizer, thus demonstrating that the EM-based BO is well-suited to the challenge of high power designs.

Index Terms—Bayesian optimization, broadband, drain waveforms, Gaussian process, high efficiency, power amplifier.

I. INTRODUCTION

Modern wireless communication systems, which have high transmission rates and spectral efficiency, are creating a growing demand for broadband high-efficiency power amplifiers (PAs). A series of high-efficiency PA modes, such as Class-F and Class-J, have been developed over the past few decades [1]-[5]. These new PA modes have received widespread interest since they allow designers to analyze PA performance from the point of view of waveform engineering [6]-[7]. The non-overlapping of the drain voltage and drain current waveforms has been recognized as a key criterion in achieving high efficiency in PA designs. Based on this concept, continuous modes have been studied in order to give greater flexibility to the drain waveforms for wideband designs [8]-[12]. In practical PA designs, the desired waveforms are often achieved through tuning the harmonic impedances presented by the matching networks. In order to realize a broadband design, the matching networks require multiple sections of transmission lines, generally greater than 10 in total.

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framework of Bayesian optimization, which is composed of Bayesian inference, modeling, prediction, and acquisition functions. Section IV describes the proposed automatic optimization-oriented strategy for PA design. Section V validates the proposed method through the presentation of 10 W and 30 W broadband high-efficiency PA designs. In this section, the algorithm convergence and measured results are shown to demonstrate that the proposed method is well-suited to broadband high-efficiency PA designs. Comparison designs using the ADS simulated annealing optimizer are also provided. Finally, in Section VI, conclusions are given.

II. HIGH-EFFICIENCY PA THEORY

The high efficiency power amplifier modes, for instance the Class-F/F-1, achieve high efficiency due to the non-overlapping voltage and current waveforms present at the internal drain. It is of no doubt that waveform engineering has become a critical guiding principle in high-efficiency PA designs. In order to fully understand how the drain voltage, \( v_d(t) \), and drain current, \( i_d(t) \), significantly affect the efficiency, it is necessary to analyze the power conversion in a PA system. In Fig. 1, the PA receives power both from the input signal and the DC supply. Since the input power is assumed to be totally dissipated in the input network and the transistor, it does not contribute directly to the output power. In (1), the DC power supplied to the device exits the device in three parts: in the form of heat dissipated in the active devices, \( P_{\text{diss}} \), as a radio frequency (RF) signal at the fundamental, \( P_{\text{out}, f} \), and RF signals at the harmonics, \( P_{\text{out}, \text{sf}} \). As a consequence, the drain efficiency, \( \eta \), is calculated by (2), and \( P_{\text{diss}} \) and \( P_{\text{out}, \text{af}} \) are defined in (3)-(4):

\[
P_{\text{DC}} = P_{\text{diss}} + P_{\text{out}, f} + \sum_{n=2}^{\infty} P_{\text{out}, \text{af}} \quad (1)
\]

\[
\eta = \frac{P_{\text{out}, f}}{P_{\text{diss}} + P_{\text{out}, f} + \sum_{n=2}^{\infty} P_{\text{out}, \text{af}}} \quad (2)
\]

\[
P_{\text{diss}} = \frac{1}{T} \int_0^T v_d(t) i_d(t) dt \quad (3)
\]

\[
\sum_{n=2}^{\infty} P_{\text{out}, \text{af}} = \frac{1}{2} \sum_{n=2}^{\infty} V_n I_n \cos(\theta_n), \quad (4)
\]

where \( V_n \) and \( I_n \) represent the drain voltage and drain current at the \( n^{th} \) harmonic with intersection angle, \( \theta_n \). It was pointed out by Colantonio that a PA with zero dissipated power, i.e., non-overlapping drain waveforms, may only have an efficiency of approximately 80%, depending on the waveforms. The ideal 100% efficiency requires (3)-(4) to satisfy the following condition [2]:

\[
P_{\text{diss}} + \sum_{n=2}^{\infty} P_{\text{out}, \text{af}} = 0. \quad (5)
\]

III. BAYESIAN OPTIMIZATION

A. Bayesian Inference

In this paper, the problem of interest is to find the best design parameters for the matching networks such that the PA achieves high efficiency. The optimization problem can be viewed as a probability event \((x, y)\), where \( x \in \mathbb{R}^d \) denotes the design parameters and \( y \) denotes the observed PA efficiency. Suppose that the 'black-box' function is defined as

\[
f(x; w) = \sum_{i=1}^{n} w_i \phi(x_i), \quad (6)
\]

where \( w=(w_1,w_2,\ldots,w_m) \) is a weighting vector, and \( \phi(x) \) is a feature space mapping. Given a set of training design parameters, the accumulated observations are \( D=[x_1,y_1],\ldots,[x_n,y_n] \).

Let's define \( p(w) \) as the prior probability which represents the belief about \( f(x;w) \) prior to the observation of \( D \), and \( p(D|w) \) as the likelihood function which expresses how probable high efficiency is given the function \( f(x;w) \). According to Bayes' theorem, the posterior probability \( p(w|D) \) for the function after observing \( D \) takes the form:

\[
p(w | D) = \frac{p(D | w) p(w)}{p(D)} \quad (7)
\]

\[
p(D) = \int p(D | w) p(w) dw, \quad (8)
\]

where \( p(w|D) \) indicates the probability to evaluate the weighting vector, \( w \), after the observations. Although the integrated term (8) cannot be expressed in a closed-form, it is a normalization constant. Thus, the posterior probability is proportional to the likelihood of \( D \) given \( w \) multiplied by the prior probability of \( w \):

\[
p(w | D) \propto p(D | w) p(w). \quad (9)
\]

In practice, the maximum a posteriori (MAP) metric is a common approximation used to estimate (9), and is written in the following form [19]:

![Fig. 1. Power conversion in a PA system.](Image 314x677 to 325x718)

![Image 334x668 to 364x752]
where \( \omega \) is the weighting vector determined by fitting (6) to the training data. When training the model, an error term is introduced to measure the misfit between the function \( f(x;w) \) and the training data. One simple error function, which is widely applied [19], is given by

\[
E(w) = \frac{1}{2} \sum_{i=1}^{n} \left( y_i - w^T \phi(x_i) \right)^2 + \frac{\lambda}{2} \|w\|^2,
\]

(11)

where \( \lambda \) is a penalty term used to control the over-fitting phenomenon, called regularization, and \( \lambda \) governs the balance between the sum-of-squares error term and the regularization term.

As can be seen from (10), the probability distribution plays a critical role in Bayesian optimization. There are many distributions which can be used to form building blocks for complex models, such as the beta distribution, the Dirichlet distribution, and the exponential family distribution [19]. Fig. 2 describes the variation in correlation with the distance between two points for the exponential family distribution. The correlation decreases when two points move far away, while it increases when they are close. In the case of \( p=2 \), it is called a Gaussian distribution, which has a smoothness correlation over different points. The Gaussian distribution is written in the following form:

\[
N(x | \mu, \sigma^2) = \frac{1}{\sqrt{2\pi\sigma}} \exp\left(-\frac{1}{2\sigma^2}(x-\mu)^2\right).
\]

(12)

Due to these good properties, a Gaussian distribution is well-suited for the distribution of continuous variables in a high-dimensional space [20]. Assuming that the observed high efficiency, \( y \), has a Gaussian distribution with a mean equal to \( f(x;w) \), given the design parameters, \( x \), then the likelihood function of (10) is expressed as

\[
\prod_{i=1}^{n} p(y_i | f(x_i;w)) = \prod_{i=1}^{n} N(y_i | f(x_i;w), \beta^{-1}),
\]

(13)

where \( \beta \) is the inverse variance of the Gaussian distribution. The prior probability is commonly selected as a Gaussian distribution with a mean equal to 0 and an inverse variance of \( \alpha \), and can be written as

\[
p(w) = N(w | 0, \alpha^{-1}).
\]

(14)

Substituting (12) to (13)-(14), the negative log likelihood function for (13)-(14) are derived as

\[
-\ln \prod_{i=1}^{n} p(y_i | f(x_i;w)) = \frac{\beta}{2} \sum_{i=1}^{n} \left( y_i - w^T \phi(x_i) \right)^2 - \frac{n}{2} \ln \beta + \frac{n}{2} \ln(2\pi)
\]

(15)

\[
-\ln p(w) = \frac{\alpha}{2} w^T w - \frac{n}{2} \ln \alpha + \frac{n}{2} \ln(2\pi).
\]

(16)

Similarly, taking the negative logarithm of (10) and combining with (15)-(16), the maximum of the posterior probability is equal to the minimum of

\[
\frac{\beta}{2} \sum_{i=1}^{n} \left( y_i - w^T \phi(x_i) \right)^2 + \frac{\alpha}{2} w^T w.
\]

(17)

Comparing (11) with (17), we can see that maximizing the posterior probability is equivalent to minimizing the error function with the regularization parameter \( \lambda = \alpha / \beta \). As a consequence, an accurate model for the PA system can be constructed based on the training data. This regression method is known as Gaussian processes regression (GPR), fitting the training data with a mean \( m(x) \) and a standard deviation \( \sigma(x) \), as depicted in Fig. 3.

2) Prediction from GP Model

A Gaussian process is a probability distribution over the function \( f(x;w) \) such that the values of \( f(x;w) \) evaluated at different points jointly have a Gaussian distribution. Suppose that \( x \) can be separated into two joint Gaussian distributed subsets, \( x_a \) and \( x_b \).
\[ x = \begin{pmatrix} x_a \\ x_b \end{pmatrix} \]  

\[ \mu = \begin{pmatrix} \mu_a \\ \mu_b \end{pmatrix} \]  

\[ \Sigma = \begin{pmatrix} \Sigma_{aa} & \Sigma_{ab} \\ \Sigma_{ba} & \Sigma_{bb} \end{pmatrix} \]  

where (19) and (20) represent the corresponding partitions of the mean vector \( \mu \) and the covariance matrix \( \Sigma \), respectively. The conditional distribution \( p(x_n|x_0) \) is expressed with the following mean and covariance:

\[ \mu_{b|a} = \mu_b + \Sigma_{b|a}^{-1}(x_a - \mu_a) \]  

\[ \Sigma_{b|a} = \Sigma_{b|b} - \Sigma_{b|a}^{-1}\Sigma_{a|b} \]  

When predicting the GP model for new predictions, it is necessary to add a noise term which serves as the unmatched error. Here, the function \( f(x) \) is modified to the following form:

\[ y = f(x) + \varepsilon \]  

where \( f(x) \) is specified by a mean function, \( m \), and a kernel function, \( k \), such that \( f(x) \sim N(m(x), k(x,x')) \), and the noise \( \varepsilon \) also has a Gaussian distribution \( \varepsilon \sim N(0, \sigma^2) \). According to the GP properties, the values \( y_i \) from the observation \( D = \{x_i, y_i\}_{i=1}^n \) will have a multivariate Gaussian distribution \( N(0, K) \), with the kernel matrix \( K \) given by

\[ K = \begin{pmatrix} k(x_1, x_1) & \cdots & k(x_1, x_n) \\ \vdots & \ddots & \vdots \\ k(x_n, x_1) & \cdots & k(x_n, x_n) \end{pmatrix} \]  

where the kernel function \( k(x_i, x_j) \) denotes the covariance matrix between \( x_i \) and \( x_j \). The new value, \( y_{n+1} \), which is predicted from the GP model at the next point, \( x_{n+1} \), has a joint Gaussian distribution with \( Y = (y_1, y_2, \ldots, y_n) \)

\[ Y_{n+1} \sim N \left( 0, \begin{pmatrix} K + \sigma^2 I & k \\ k^T & k(x_{n+1}, x_{n+1}) \end{pmatrix} \right) \]  

\[ k = k(x_{n+1}, x_1) \cdots k(x_{n+1}, x_n) \]  

Referring to (21) and (22), the predicted data \( (x_{n+1}, y_{n+1}) \) from the GP model is specified as a Gaussian distribution

\[ m(x_{n+1}) = k^T(K + \sigma^2 I)^{-1}Y \]  

\[ \sigma^2(x_{n+1}) = k(x_{n+1}, x_{n+1}) - k^T(K + \sigma^2 I)^{-1}k \]  

C. Acquisition Functions for Searching

After building the GP model, a non-trivial task is how to search for the next point of interest from the constructed GP model. Acquisition functions are exploited to determine where to next evaluate the GP model. Since our goal is to find a set of design parameters such that high efficiency is obtained, the objective function is defined as

\[ x^* = \arg\max_{x \in \mathcal{R}^p} y(x) \]  

In Bayesian optimization, there are four commonly used acquisition functions: probability of improvement (PI), expected improvement (EI), upper confidence bound, and entropy search [24]-[27]. In this paper, the first two acquisition functions are applied in order to search for the optimal values for the design parameters.

1) Probability of Improvement

PI evaluates \( y(x) \) at the point where the improvement will most likely occur [24]. However, PI focuses on local optimization such that the points have high uncertainty. Given the current maximum point \( x' \), PI is calculated by

\[ \text{PI}(x) = p(y(x) \geq y(x')) \]  

\[ = \Phi \left( \frac{m(x) - f(x')}{\sigma(x)} \right), \]  

where \( f(x') \) represents the current best value, \( m(x) \) and \( \sigma(x) \) are calculated from (27)-(28), and \( \Phi(\cdot) \) is the normal cumulative distribution function.

2) Expected Improvement

EI has been shown to be an efficient criterion for finding the global optimum in many 'black-box' functions [25]. It allows us to make a trade-off between the local optimization and the global search. This acquisition can be computed analytically as

\[ \text{EI}(x) = \left[ (m(x) - f(x')) \Phi(Z) + \sigma(x) \phi(Z) \right] \text{if } \sigma(x) > 0 \]  

\[ = 0 \text{ if } \sigma(x) = 0 \]  

\[ Z = \frac{m(x) - f(x')}{\sigma(x)}, \]  

where \( \phi(\cdot) \) denotes the probability density function. In (31), the first term chooses the points where the mean is high while the second term targets the points where the variance is large.

Considering constraint conditions for the objective function, PI can be used to calculate the probability of being greater than the constraint limit [24]. In this situation, one model is built for the objective function; the other is for the constraint function. Since the two models are independent, the new point in (29) is obtained by maximizing

\[ \begin{array}{c}
\end{array} \]
\[ x^* = \arg \max_{x \in X} [EI(x)PI(x)]. \] (33)

Table I summarizes the algorithm steps of Bayesian optimization. It is worth noting that two major choices significantly affect the global search of Bayesian optimization. The first is selecting an appropriate GP distribution and a kernel function for the modeling; the second is choosing an acquisition function to guide the search.

IV. OPTIMIZATION-ORIENTED STRATEGY

The automatic optimization strategy for the PA designs is accomplished by combining ADS with MATLAB or R. As is shown in Fig. 4, ADS is used to run the PA simulation so that training data is obtained. MATLAB or R is responsible for the Bayesian optimization since there are many related codes written in these two programming languages, which are available for public use. MATLAB or R starts the Bayesian optimization when the training data is exported from ADS, Similarly, ADS activates the simulation as soon as it receives new design parameters. Finally, the optimization strategy outputs the best design parameters to the users after the iterations finish.

In this paper, the design parameters are the widths and the lengths of the matching network transmission lines. The simplified real frequency technique (SRFT) is exploited to determine the matching network topology and the initial guess for the design parameters [28]-[29]. The S-parameters of the transistor are used as the inputs to the SRFT in order to generate this initial guess. As shown in Fig. 5, there are six stepped-impedance transmission lines and a bias line for both the input and output matching networks, resulting in 28 design parameters in total. The output targets in the optimization are the fundamental output power, and the harmonic and dissipated components described in Section II. It is important to note that it is the voltage and current waveforms at the current-generator plane that we are interested in optimizing, rather than the waveforms seen at the package plane. The objective function is defined as the root mean square (RMS) of the fundamental output power across the band:

\[ y_1 = \sqrt{\frac{1}{m} \sum_{i=1}^{m} (P_{\text{out},f})^2}, \] (34)

where \( m \) denotes the number of the frequency points. The constraint function is the sum of the RMS of the harmonic and dissipated power:

\[ y_2 = \sqrt{\frac{1}{m} \sum_{i=1}^{m} \sum_{n=2}^{5} (P_{\text{out},n})^2} + \sqrt{\frac{1}{m} \sum_{i=1}^{m} (P_{\text{diss}})^2}. \] (35)

Harmonics greater than the fifth are neglected in this paper due to their minor impact upon efficiency improvement. As per (2) and (5), this paper optimizes the drain waveforms by maximizing (34) while minimizing (35), guaranteeing that high efficiency is achieved in a wide band.

Since the 28-dimensional space is very high, it requires a vast number of training data to capture the characteristics of the PA system, leading to quite expensive computation during the optimization. In order to balance the model accuracy and the computational time, this paper uses sub-models to search for design parameters instead of using the whole model. A sub-model is constructed around the current point, representing a portion of the whole model. After finding a better point, a new sub-model is constructed and is then applied to predict the next point. In this way, the sub-models are able to find the optimum point for the whole model. However, it should be pointed out that there is a risk of the optimization converging on a local minimum, rather than the desired global minimum.

Fig. 6 further describes the optimization process more specifically for the PA designs. First, we set the iteration number and apply the SRFT to obtain the initial guess for the design parameters. The sampling points are created within a ±10% range of the current point using the Latin hypercube sampling technique [30]. By sweeping the sampling design parameters, ADS runs the simulation and exports the RMS of those in (34) and (35). With the available training data, the optimization algorithm builds the GP sub-models and predicts new design parameters by maximizing the acquisition function...
in (33) using the DIRECT algorithm [31]. For the sake of good convergence, the optimization algorithm rejects predicted points which worsen performance and keeps the current best one. In order to avoid sub-optimal local minima in the optimization, the random seed is changed such that the sampling points and the sub-models are renewed at each iteration. The optimization algorithm repeats the above steps and finally outputs the optimal design parameters when the iterations are terminated. In this way, we apply Bayesian optimization to optimize a PA design automatically.

V. PRACTICAL PA OPTIMIZATION

A. Broadband 10 W PA Design

In order to demonstrate the validity of this design approach, we optimized the designs of broadband high-efficiency 10 W PA using circuit-based and EM-based Bayesian optimization, respectively. Cree CGH40010F Gallium Nitride (GaN) high-electron mobility transistors (HEMTs) were used for the designs. In the circuit-based BO, the sub-models were built with the training data from circuit simulation. Similarly, in the EM-based BO, EM simulation supplied the training data for building the sub-models, which are more accurate than those of the circuit simulation. Considering that the EM simulation requires expensive computational time, we used 50 training data points to build the GP sub-models in the EM-based BO. In order to test the sensitivity of the optimization to the number of training data, 50 and 200 training data points were used in the circuit-based BO. For comparison, the ADS simulated annealing optimizer with 1000 iterations was employed to optimize the PAs while keeping the same variable settings and objective functions.

The transistor model is a critical part of the ‘black-box’ modeling, affecting the deviation between the simulated results and measurements to a great extent. Clearly, if the model is not of sufficient accuracy, then the optimization process will not result in good performance upon measurement. Cree's dynamic load-line model has been shown in a large number of papers to be of good accuracy [10]-[11], and, hence, was used for the designs. Additionally, the model in question gives access to the intrinsic current and voltage waveforms, as desired.

The PA designs were implemented on Taconic RF35 substrate with $\varepsilon_r=3.5$ and a thickness of 1.52 mm. The iteration number was set to $t=20$ and the initial design parameters of the matching networks of Fig. 5 were obtained using the SRFT, $x_0 = [W_1, W_2, \ldots, W_{14}, L_1, L_2, \ldots, L_{14}]$, where $W_{1,14} = [5.75, 1.63, 15.30, 2.21, 27.91, 26.15, 2.09, 11.34, 1.57, 11.95, 1.83, 5.78, 1, 1]$ mm, and $L_{1,14} = [5, 5, 5, 5, 5, 5, 5, 5, 5, 5, 5, 5, 20, 20]$ mm. The bounded range for the width in the matching networks was 0.7 mm $< W < 30$ mm, corresponding to the characteristic impedance within $9 \Omega < Z_c < 105 \Omega$ on the RF35 board. Following the optimization process shown in Fig. 6, the 10 W PAs were optimized from 1.5 GHz to 2.5 GHz, with a step of 50 MHz. The optimization process ran on a computer featuring an Intel Core i7-3770 CPU @ 3.40 GHz with 16.0 GB RAM.

After 20 iterations, the EM-based BO outputted the fabricated 10 W PA designs. (a) EM-based BO with 50 training data points. (b) Circuit-based BO with 50 training data points. (c) Circuit-based BO with 200 training data points.

Fig. 7. Fabricated 10 W PA designs. (a) EM-based BO with 50 training data points. (b) Circuit-based BO with 50 training data points. (c) Circuit-based BO with 200 training data points.
optimized parameters, \( W_{1-14} = [5.07, 1.51, 15.38, 2.79, 22.69, 26.12, 2.16, 11.19, 1.60, 10.63, 1.43, 5.96, 1.28, 1.11] \ mm, \) and \( L_{1-14} = [5.26, 5.55, 5.68, 3.41, 7.67, 6.33, 4.25, 7.89, 4.98, 4.02, 5.43, 4.67, 18.89, 19.22] \ mm. \)

As for the circuit-based BO with 200 training data points, we got the optimized parameters, \( W_{1-14} = [5.01, 1.60, 20.14, 3.02, 26.05, 28.62, 2.10, 12.62, 1.57, 12.81, 1.64, 4.97, 1.32, 1.03] \ mm, \) and \( L_{1-14} = [4.82, 4.55, 5.10, 2.96, 7.05, 7.16, 4.07, 9.80, 5.88, 4.46, 5.09, 5.37, 22, 21.04] \ mm. \)

Fig. 7 shows the fabricated PAs which were optimized using the three approaches described above. The PAs were biased at a drain voltage of 28 V with a quiescent drain current of 70 mA. The convergence of the proposed optimization algorithm is shown in terms of a normalized error in Fig. 8. The normalized errors drop considerably over the 20 iterations, indicating that the algorithm converges very quickly. It was observed that the normalized error reduced when the training data number was increased from 50 to 200. Therefore, increasing the training data number is helpful in obtaining better design parameters from accurate models. The simulated drain waveforms of the PA using EM-based BO, depicted in Fig. 9, show only a small amount of overlap, suggesting high performance.

Fig. 10 shows the measurement results of the optimized PAs, together with comparison designs created using the SRFT and the ADS simulated annealing optimizer. These results were obtained with the measurement setup shown in Fig. 11, when an available input power of 28 dBm was provided across the band. The optimized PAs all showed large improvements compared with the initial design using the SRFT. The optimized PA using EM-based BO delivered drain efficiency above 60.0% from 1.5 GHz to 2.5 GHz, with output power greater than 39.9 dBm and gain larger than 11.8 dB. The PA using circuit-based BO with 200 training data points showed better performance than that using 50 training data, corresponding to the lower normalized error of Fig. 8. It achieved greater than 60.8% drain efficiency over the entire band, with output power greater than 39.8 dBm and gain larger than 11.4 dBm. Although the ADS simulated annealing optimizer achieved a drain efficiency of greater than 61.4% over the band, the output power decreased to 39.1 dBm, with gain dropping down to 11.0 dB. It should be noted that the EM-based design provided superior output power and gain when compared with the other designs, particularly at the upper end of the frequency scale.

Fig. 11. Measurement setup.
B. Broadband 30 W PA Design

We also applied the above approaches to optimize the designs of 30 W PA operating from 1.5 GHz to 2.5 GHz. Cree CGHV40030F GaN HEMTs were used for the designs. Again, the Cree’s dynamic load-line model was utilized in the ADS simulation. The SRFT provided the initial design parameters, 

\[ W_{1-14} = [7.01, 1.44, 20.79, 3.07, 26.86, 30, 1.66, 20.29, 4.01, 17.61, 3.08, 3.66, 0.8, 0.8] \text{mm}, \]

and 

\[ L_{1-14} = [7, 7, 7, 7, 7, 7, 6.5, 6.5, 6.5, 6.5, 6.5, 15, 20] \text{mm}. \]

As shown in Fig. 12, the first PA using EM-based BO had the dimensions of 

\[ W_{1-14} = [5.51, 1.84, 11.71, 1.50, 19.14, 24.64, 1.67, 17.57, 3.50, 18.40, 2.63, 6.51, 1.03, 1.17] \text{mm}, \]

and 

\[ L_{1-14} = [3.37, 3.92, 5.65, 3.20, 11.79, 5.20, 3.34, 8.59, 8.92, 5.43, 8.28, 8.15, 20.21, 20.49] \text{mm}. \]

The second PA using circuit-based BO with 50 training data points had the optimized parameters, 

\[ W_{1-14} = [8.33, 1.59, 17.11, 2.61, 27.13, 28.98, 1.55, 19.85, 3.62, 18.30, 2.85, 5.90, 1.04, 1.22] \text{mm}, \]

and 

\[ L_{1-14} = [6.82, 7.41, 5.77, 6.60, 8.09, 6.96, 4.14, 7.60, 7.8, 5.77, 7.86, 7.12, 18.68, 15.59] \text{mm}. \]

As for the last design using circuit-based BO with 200 training data points, the optimized parameters were, 

\[ W_{1-14} = [8.33, 1.59, 17.11, 2.61, 27.13, 28.98, 1.55, 19.85, 3.62, 18.30, 2.85, 5.90, 1.04, 1.22] \text{mm}, \]

and 

\[ L_{1-14} = [6.82, 7.41, 5.77, 6.60, 8.09, 6.96, 4.14, 7.60, 7.8, 5.77, 7.86, 7.12, 18.68, 15.59] \text{mm}. \]
power, as well as minimize the harmonic and dissipated programming. Its aim is to maximize the fundamental output automatically by combining ADS with MATLAB or R from 1.5 GHz to 2.5 GHz. This method is implemented paper to design broadband high-frequency power amplifiers.

![Fig. 15. Simulated intrinsic drain waveforms at 2 GHz for the 30 W PA design using EM-based BO.](image)

<table>
<thead>
<tr>
<th>Method</th>
<th>DE (%)</th>
<th>Pout (dBm)</th>
<th>Cost Time (min / iteration)</th>
</tr>
</thead>
<tbody>
<tr>
<td>EM-BO, 50 Data</td>
<td>&gt; 57.0</td>
<td>&gt; 43.8</td>
<td>- 70</td>
</tr>
<tr>
<td>Circuit-BO, 50 Data</td>
<td>&gt; 49.2</td>
<td>&gt; 41.0</td>
<td>- 3</td>
</tr>
<tr>
<td>Circuit-BO, 200 Data</td>
<td>&gt; 51.4</td>
<td>&gt; 41.4</td>
<td>- 4</td>
</tr>
<tr>
<td>ADS Simulated Annealing</td>
<td>&gt; 49.5</td>
<td>&gt; 40.6</td>
<td>- 0.2</td>
</tr>
</tbody>
</table>

The measurements are compared from 1.5 GHz to 2.5 GHz. The cost time is the approximate average time for each optimization iteration.

The algorithm convergence of the 30 W PA designs are shown in Fig. 13. The 30 W PAs were biased at a drain voltage of 50 V with a quiescent drain current of 95 mA, and were measured with 32 dBm available input power. As shown in Fig. 14, the optimized PA using EM-based BO obtained drain efficiency greater than 57.0% across the band, with output power greater than 43.8 dBm and gain larger than 11.6 dBm. As high power PAs have a smaller range of ideal harmonic impedances, circuit-based optimization designs show worse performances. The PA using the circuit-based BO with 200 training data gave a minimum drain efficiency of 51.5%, which was still better than that of using the ADS simulated annealing optimizer. The EM-based PA again showed significantly greater output power and gain than the other designs. The non-overlapping drain waveforms for the 30 W PA design using EM-based BO are shown in Fig. 15.

Table II gives the comparison of the 30W PAs using the above different methods. From the compared results, we can see that EM-based BO is more preferable than circuit-based BO in high power designs although it requires expensive computational time.

**VI. CONCLUSIONS**

A Bayesian optimization-based method is presented in this paper to design broadband high-frequency power amplifiers from 1.5 GHz to 2.5 GHz. This method is implemented automatically by combining ADS with MATLAB or R programming. Its aim is to maximize the fundamental output power, as well as minimize the harmonic and dissipated components, resulting in similar non-overlapping drain waveforms. The optimization algorithm builds the GP sub-models for the PA and utilizes an acquisition function to predict design parameters with which high efficiency is achieved. The 10 W and 30 W PA designs were optimized using circuit-based BO and EM circuit-based BO. Measured results show that the 10 W PA using circuit-based BO with 200 training data points obtained better than 60% drain efficiency and greater than 39.8 dBm output power from 1.5 GHz to 2.5 GHz. In the 30 W PA designs, EM-based BO performed much better than circuit-based BO, due to the higher power designs being more sensitive to simulation accuracy. The 30 W PA using EM-based BO operated at a drain efficiency of greater than 57%, with output power greater than 43.8 dBm across the band. Comparison results show that the proposed strategy greatly surpasses the ADS simulated annealing optimizer in terms of the gain and output power achieved across the band, with comparable efficiency shown with both methods. Moreover, it should be stressed that the performances of the optimized PAs also rely on the accuracy of the transistor models applied in the ADS simulation.

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**REFERENCES**

His research interests include non-linear device modeling and optimization algorithms applied to power amplifier design.